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## \*BIBDATASHEET\*

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## APPLICANTS

Kazuhisa Kogure, Kawasaki, JAPAN;

Hiroshi Yamada, Kawasaki, JAPAN;

Atsushi Suda, Yokohama, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\* None

\*\* FOREIGN APPLICATIONS \*\*\*\*\* Yes

JAPAN 2000-0051:19 01/14/2000

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature _____ Initials _____	STATE OR COUNTRY JAPAN	SHEETS DRAWING 10	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 2
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## ADDRESS

21171

STAAS &amp; HALSEY LLP

SUITE 700

1201 NEW YORK AVENUE, N.W.

WASHINGTON, DC

20005

## TITLE

Clock regeneration circuit and optical signal receiver using the same

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